

(19)



JAPANESE PATENT OFFICE

## PATENT ABSTRACTS OF JAPAN

(11) Publication number: **10154941 A**(43) Date of publication of application: **09.06.98**

(51) Int. Cl.

**H03M 13/00**  
**G06F 11/10**(21) Application number: **08327637**(71) Applicant: **NEC CORP**(22) Date of filing: **22.11.96**(72) Inventor: **KAWASAKI YORIJI**(54) **ERROR CORRECTION CIRCUIT**

## (57) Abstract:

**PROBLEM TO BE SOLVED:** To provide a recovery device that is used to reduce a circuit scale of the error correction circuit processing an error correction code with a long code length.

**SOLUTION:** The correction circuit is provided with a memory circuit storing error correction codes before and after correction, a syndrome calculation section 1 that calculates a syndrome of before-correction data 101, an error value/ error position calculation section 2 that calculates an error value/error position from the syndrome, an XOR gate 3 that reads again data before correction from the memory and applies exclusive OR to the data and an output of the error value/error position calculation section 2, and an output control section 5 that outputs after-correction data based on output enable signal to write back the corrected data to the memory circuit.

COPYRIGHT: (C)1998,JPO

